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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hyun-kyu YUN

Application No.: 10/808,341

Group Art Unit: 2189

Confirmation No.: 5441

Filed: March 25, 2004

Examiner: Woo H. Choi

For: DSP (DIGITAL SIGNAL PROCESSING) ARCHITECTURE WITH A WIDE
MEMORY BANDWIDTH AND A MEMORY MAPPING METHOD THEREOF

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Alexandria, Virginia 22313

Sir:

In response to the Office Action mailed on November 30, 2006, Applicants provisionally elect Group I, including 1-6 and 11-18, without traverse. Please note that although the Examiner listed Group I as including claims 1-6 and 11-16, claims 17-18 are also grouped with claims 11-16, and depend from claims 11 and 14, respectively.

Respectfully submitted,

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Date: December 27, 2006

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